

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions and listings of claims in the application, and entry of this listing of claims is respectfully requested:

**Listing of Claims:**

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 1 Claim 1 (currently amended): A digital camera, comprising:

2 an image sensor having a first resolution;

3 a timing generator for reading a first resolution image signal from said image sensor;

4 a first processor for performing predetermined signal processing on said first resolution  
5 image signal;

6 an imaging device for imaging an object so as to output a first image signal having a first  
7 resolution;

8 a thinning-out circuit for thinning out ~~[[said]]~~ the first resolution image signal ~~outputted from~~  
9 ~~said first processor and creating~~ so as to create a second resolution image signal ~~[[with]]~~ having a  
10 second resolution lower than ~~[[said]]~~ the first resolution;

11 a ~~[[main]]~~ memory having at least two memory areas;

12 a selector for selectively selecting ~~alternately each of~~ said two memory areas;

13 a ~~write controller~~ writer for writing ~~[[said]]~~ the second resolution image signal outputted  
14 from said thinning-out circuit to one of said two memory areas based on ~~an output of said selector~~

15 a selection result of said selector; [[and]]

16 a ~~read controller~~ reader for reading [[a]] the second ~~resolution~~ image signal from the other  
17 of said two memory areas based on ~~an output of said selector~~ the selection result of said selector; and  
18 a displayer for displaying an image based on the second image signal read out by said  
19 reader.

AI Cont  
1 Claim 2 (currently amended): A digital camera according to claim 1, wherein said ~~timing~~  
2 ~~generator reads out~~ imaging device outputs [said] one screen of the first ~~resolution~~ image signal in  
3 ~~an amount of one screen at an interval of a~~ every first predetermined period, and said ~~read controller~~  
4 reader reading out [[said]] one screen of the second [resolution] image signal ~~in an amount of one~~  
5 ~~screen at an interval of a~~ every second predetermined period shorter than [[said]] the first  
6 predetermined period.

1 Claim 3 (currently amended): A digital camera according to claim 2, wherein ~~said second~~  
2 ~~predetermined period is 1/Nth (N is an integer equal to or greater than 2) of said first predetermined~~  
3 ~~period~~ the first period is an integral multiple of the second period.

1 Claim 4 (currently amended): A digital camera according to claim 2, wherein said selector  
2 ~~alternately~~ switches [[for]] a memory area to be selected at an interval of [[said]] the first  
3 predetermined period.

5. (canceled)

6. (currently amended): A digital camera according to claim 1, further comprising:

an instruction key; and

a ~~second~~ processor for outputting at predetermined timing a first disable signal, a second

disable signal and a third disable signal in response to an operation of said instruction key; wherein

said thinning-out circuit being disabled by ~~[[said]]~~ the first disable signal simultaneously with

the operation of said instruction key,

said ~~read controller~~ reader being disabled by ~~[[said]]~~ the second disable signal simultaneously

with the operation of said instruction key, and

said ~~write controller~~ writer writing ~~[[said]]~~ the first ~~resolution~~ image signal outputted from

said ~~first processor~~ imaging device to said ~~[[main]]~~ memory and disabled by ~~[[said]]~~ the third disable

signal after ~~[[said]]~~ one screen of the first ~~resolution~~ image signal ~~in an amount of one screen~~ has

been written.

Claim 7 (currently amended): A digital camera according to claim 6, further comprising a

recorder to record ~~[[said]]~~ the first ~~resolution~~ image signal written on said ~~[[main]]~~ memory to a

recording medium.

1            Claim 8 (currently amended): A digital camera according to claim 7, wherein said ~~second~~  
processor cancels *Phil* ~~[[said]]~~ the second disable signal from outputting after ~~[[said]]~~ the first ~~resolution~~  
3 image signal has been recorded, and said ~~read-controller~~ reader reading ~~[[said]]~~ the first ~~resolution~~  
4 image signal from said ~~main~~ memory.

1            Claim 9 (original): A digital camera according to claim 1, wherein said memory has a single  
2 signal input/output port.

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